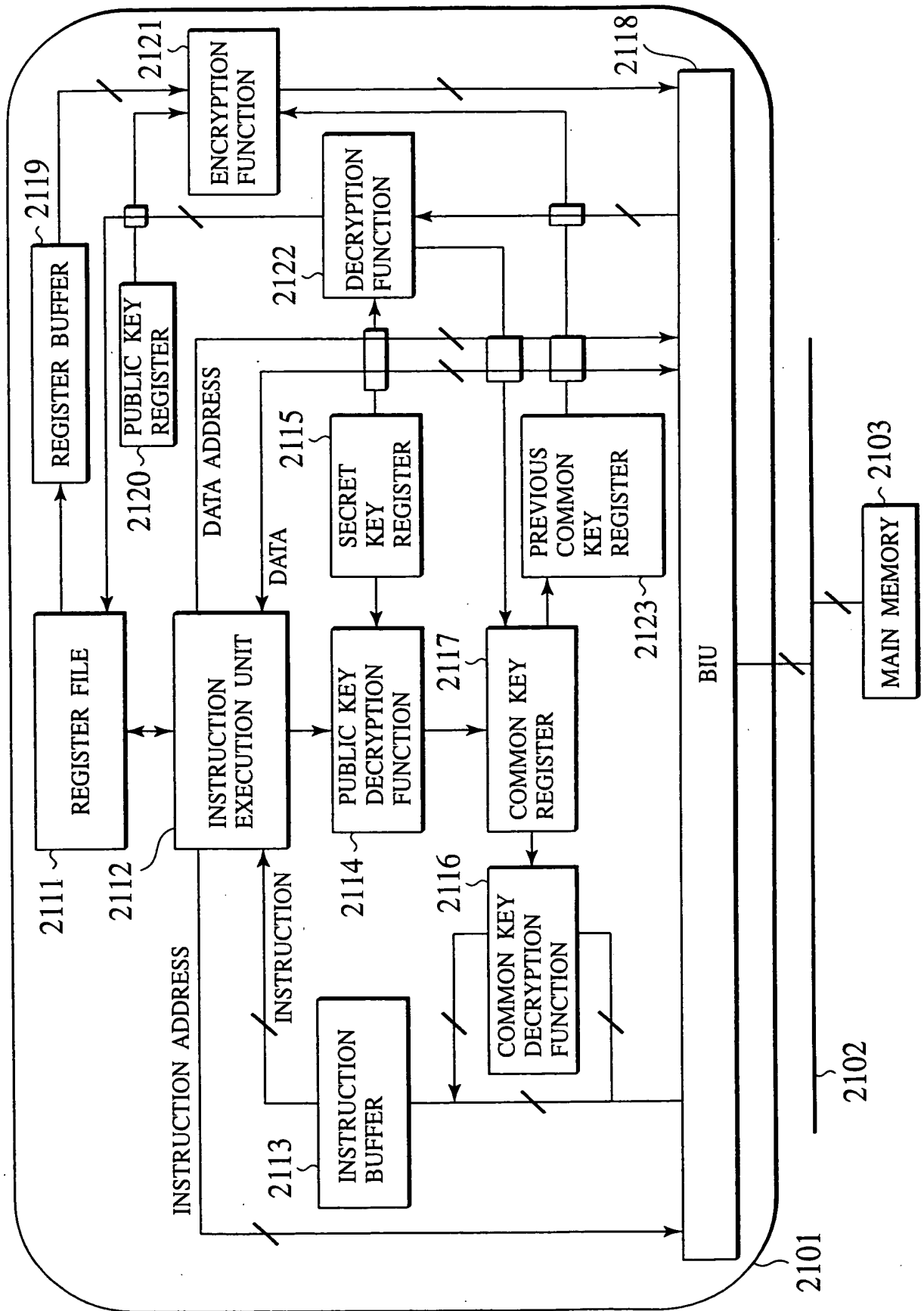


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FIG.1



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FIG.2

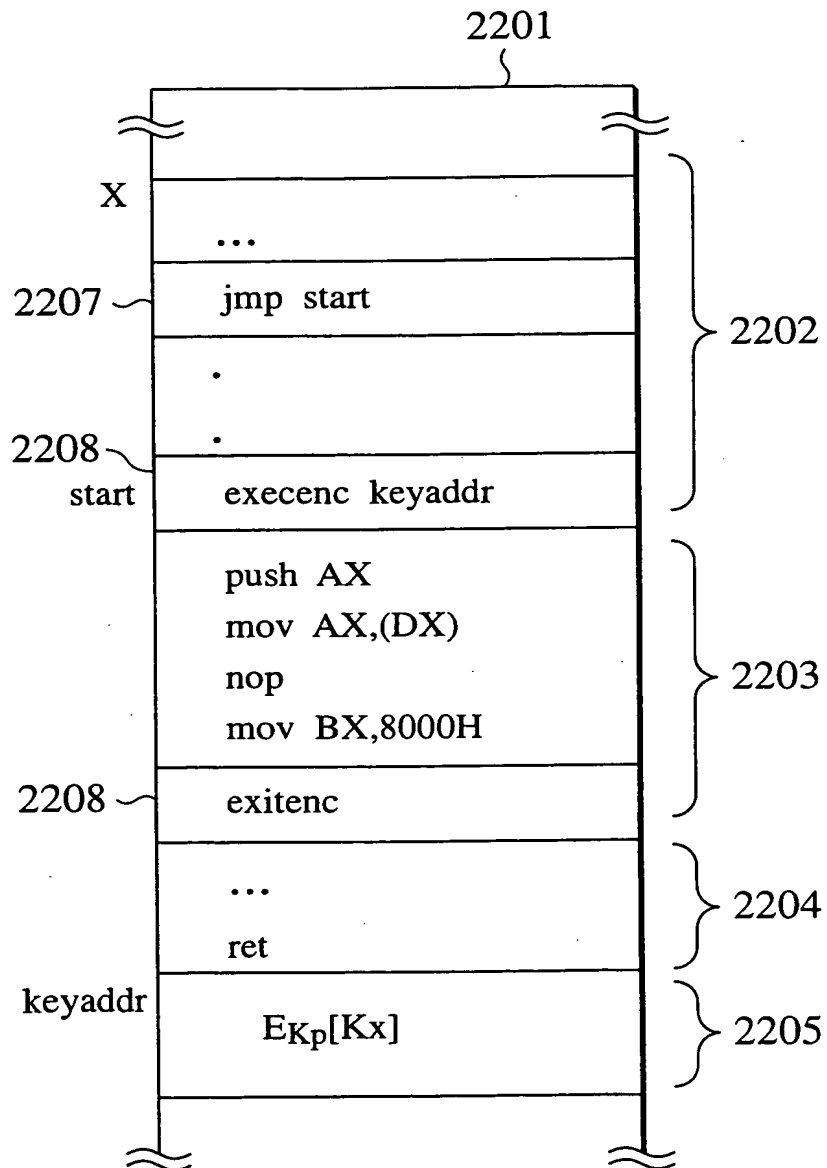
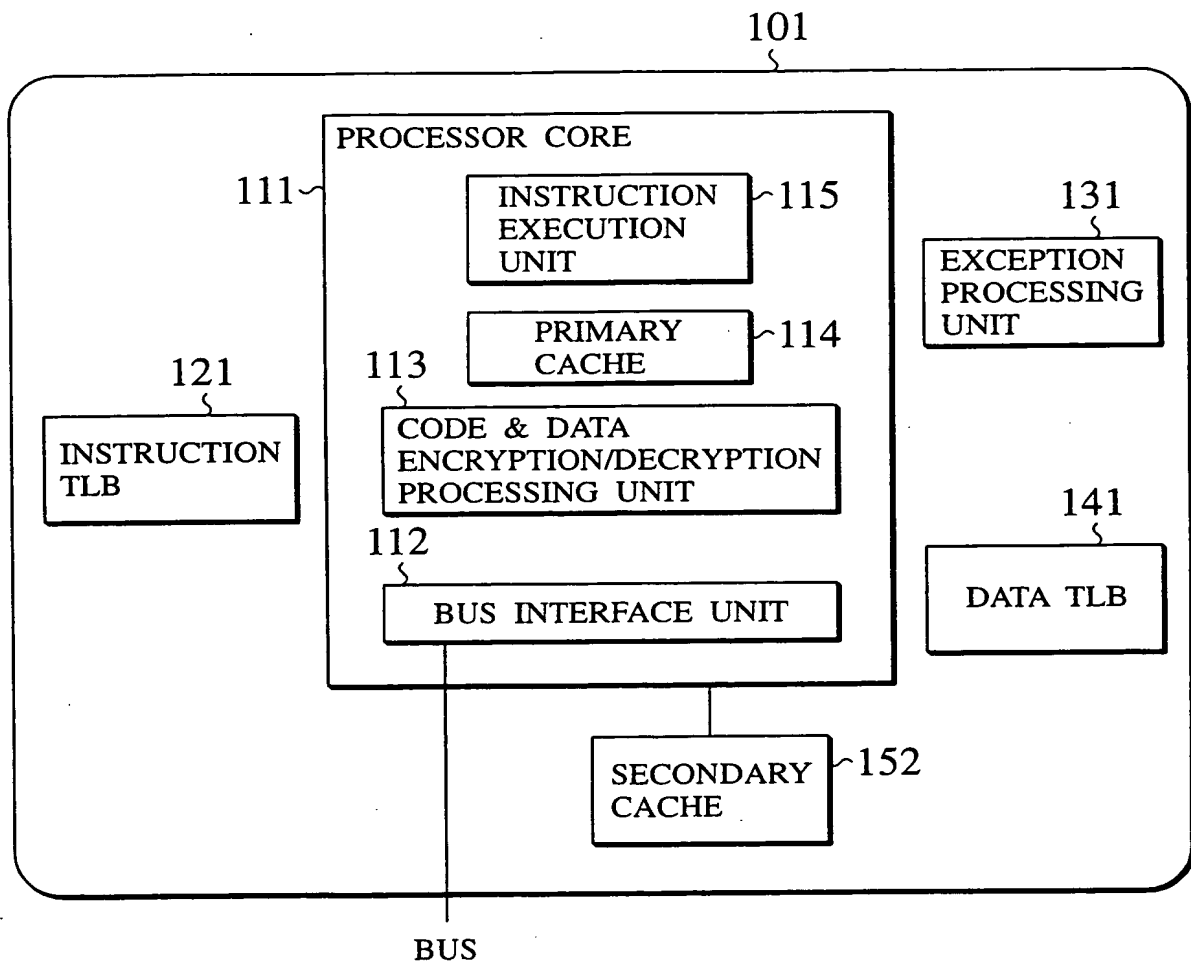


FIG.3



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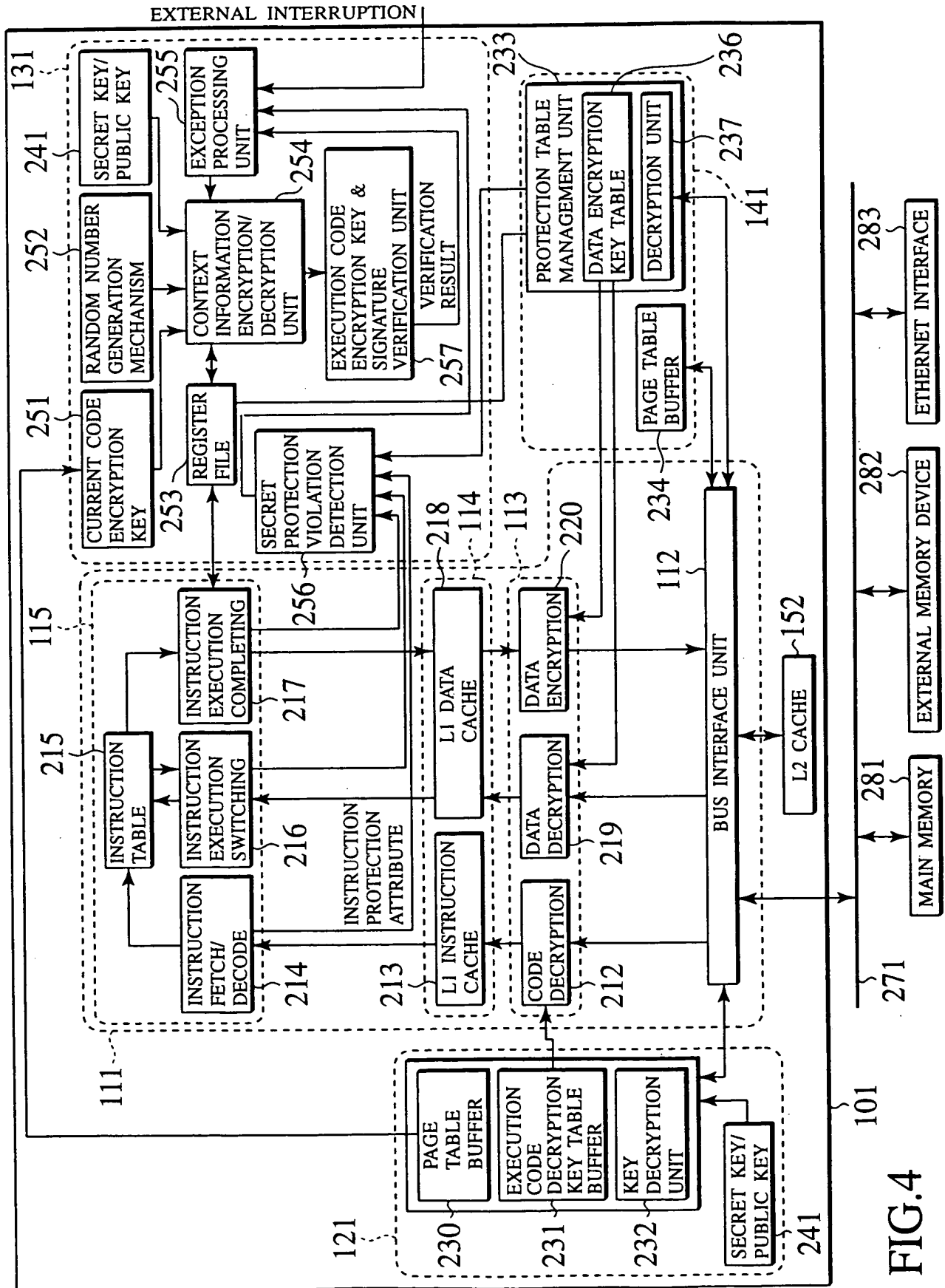
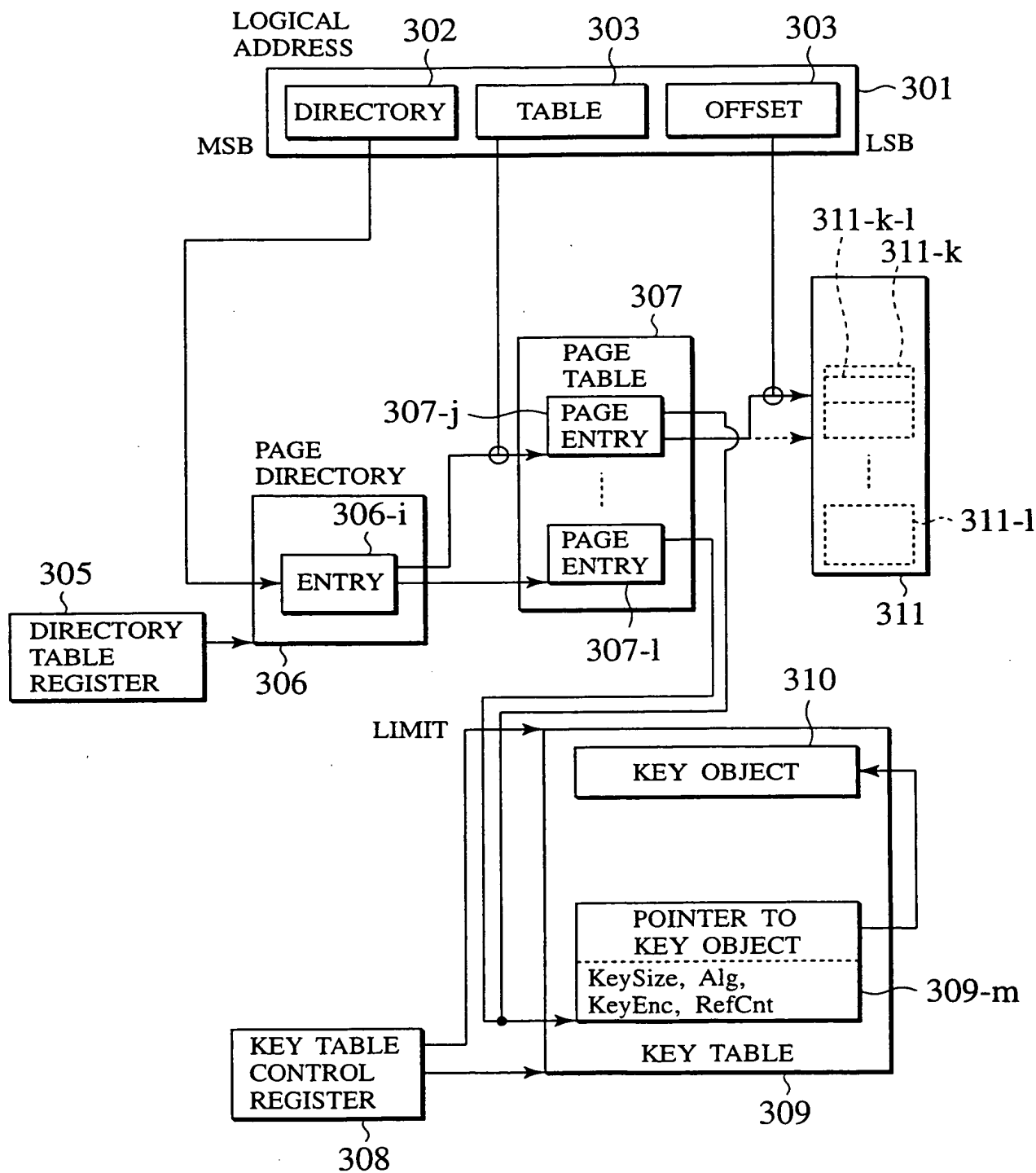


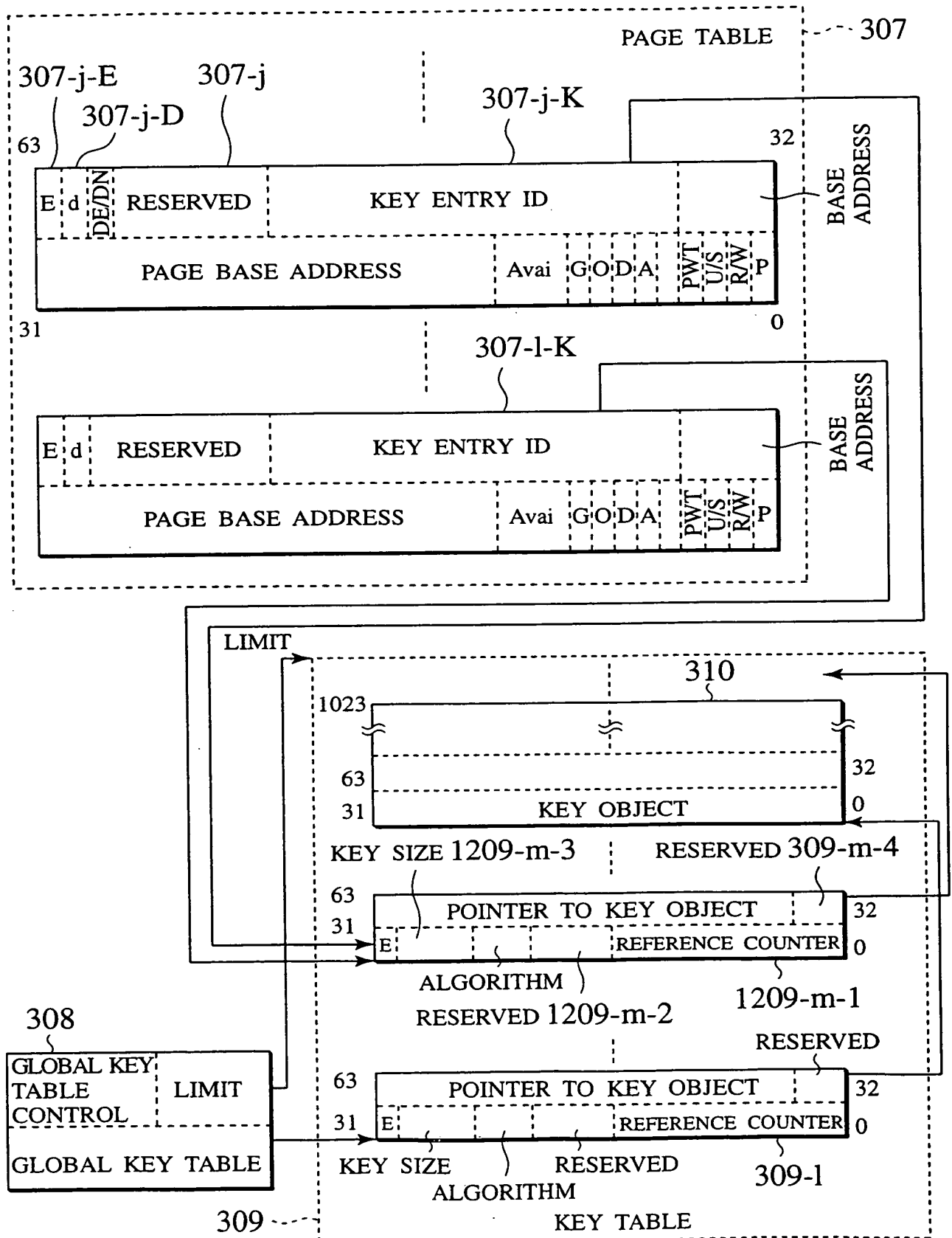
FIG. 4

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FIG.5



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FIG.6



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FIG.7A

	0	1	2	3
0	A0	A1	A2	A3
1	B0	B1	B2	B3
2	C0	C1	C2	C3
3	D0	D1	D2	D3
4	E0	E1	E2	E3
5	F0	F1	F2	F3
6	G0	G1	G2	G3
7	H0	H1	H2	H3

BEFORE INTERLEAVING

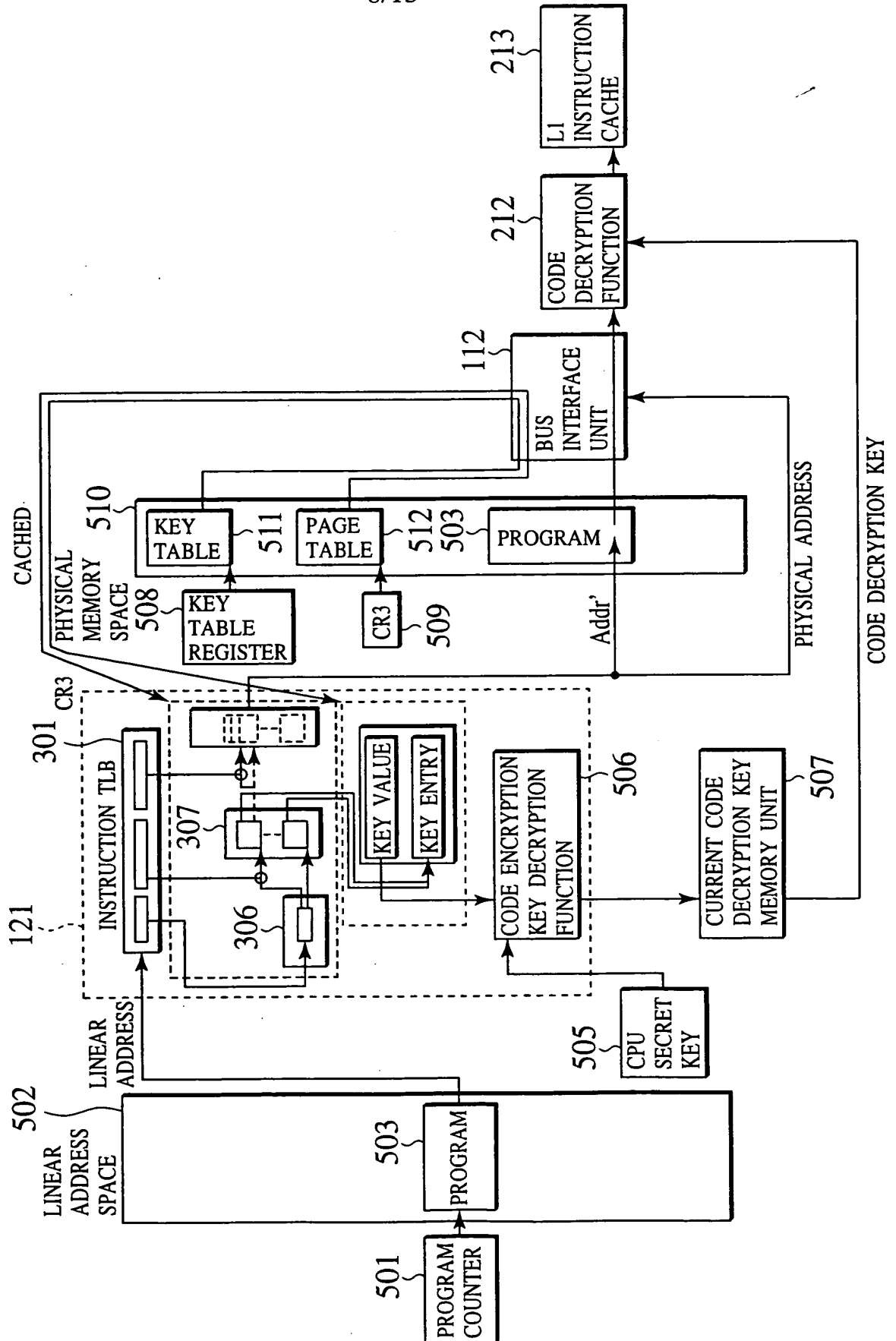
FIG.7B

0	A0	B0	C0	D0
1	E0	F0	G0	H0
2	A1	B1	C1	D1
3	E1	F1	G1	H1
4	A2	B2	C2	D2
5	E2	F2	G2	H2
6	A3	B3	C3	D3
7	E3	F3	G3	H3

AFTER INTERLEAVING

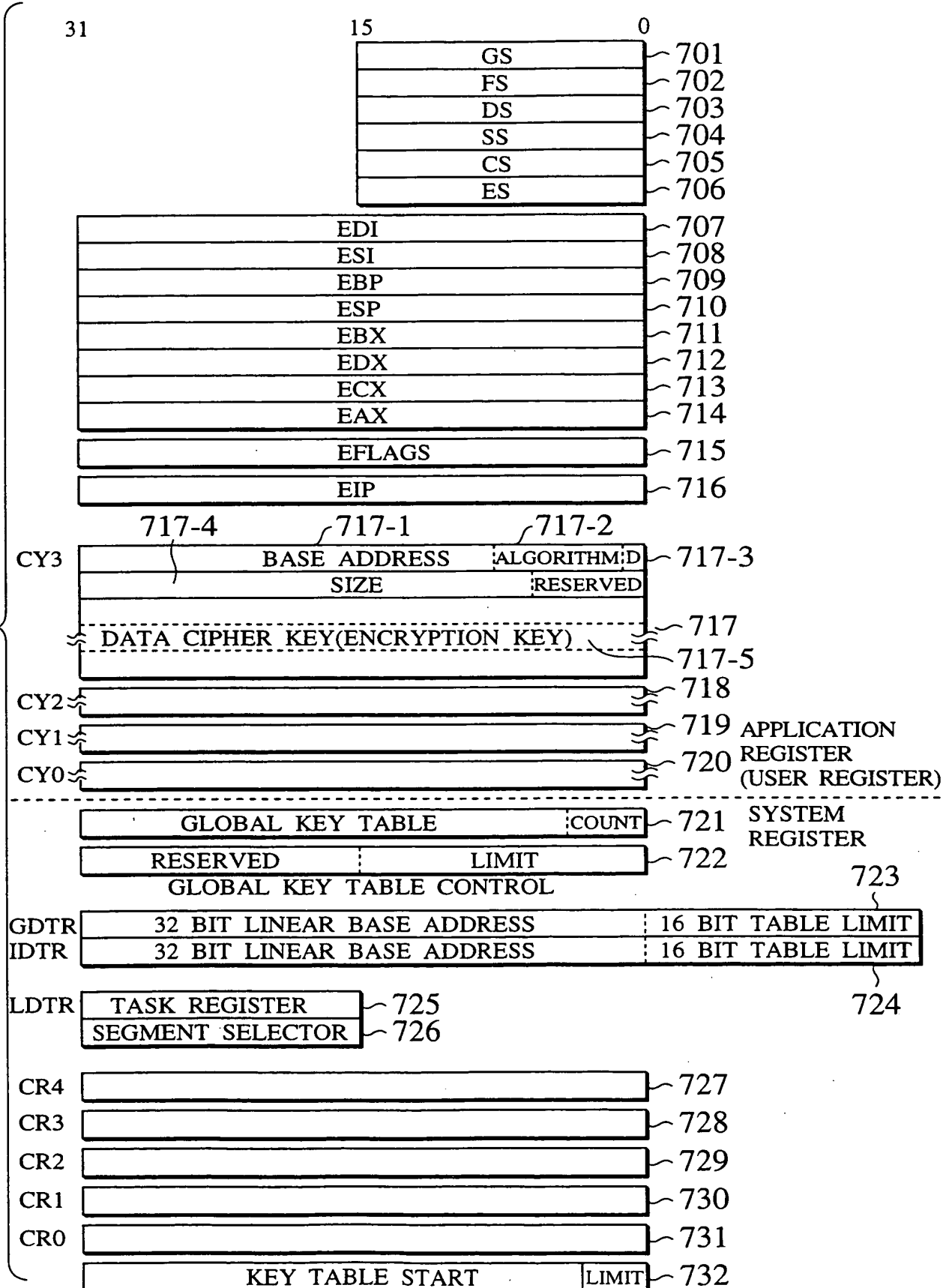
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FIG. 8



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FIG.9



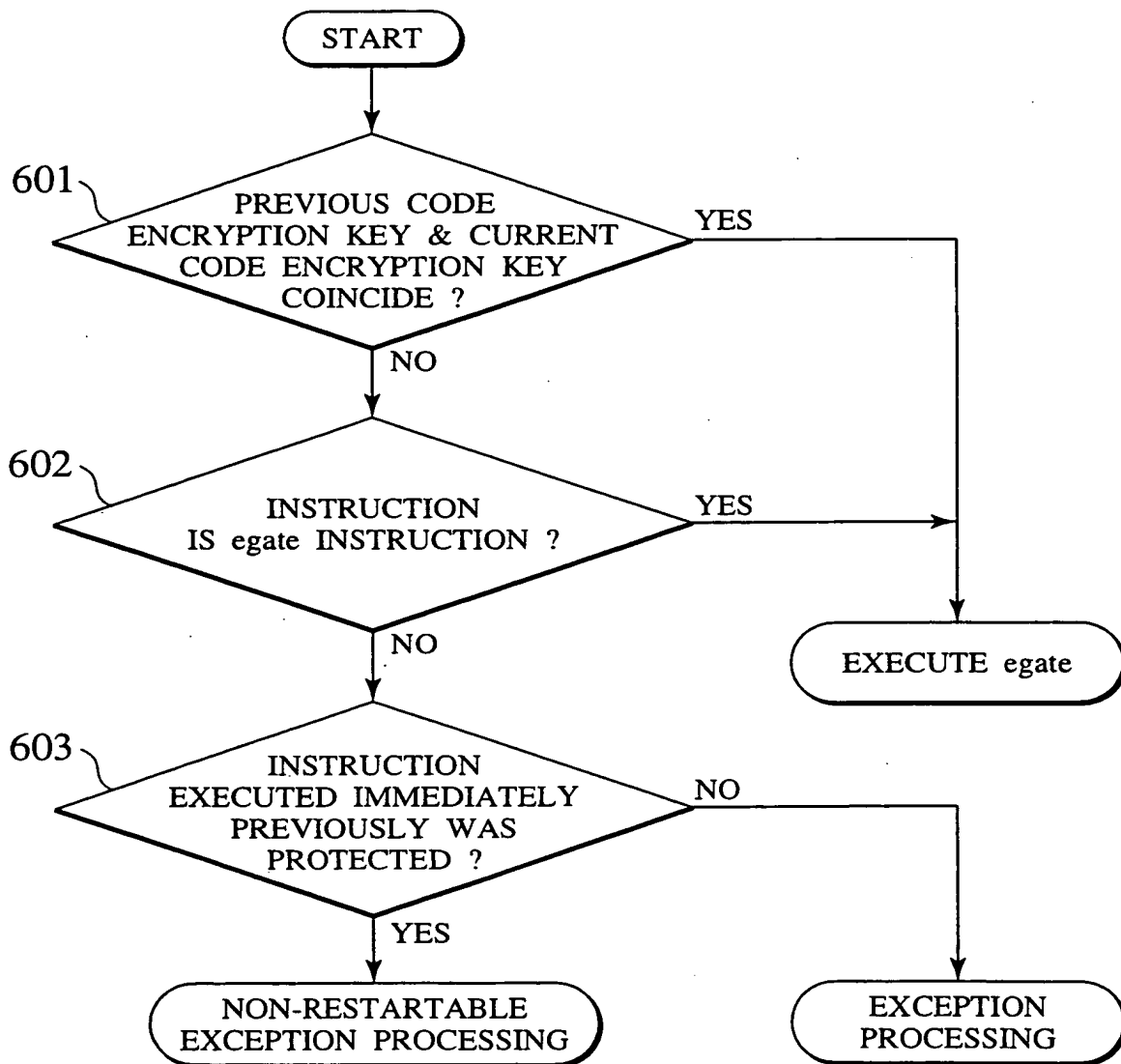
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FIG.10

S[message] by Ks		834
E[Kr] by Kp		833
E[Kr] by Kcode		832
PADDING		831
BASE ADDRESS	RESERVED E	
SIZE		
DATA ENCRYPTION KEY FOR CY3		830
DATA ENCRYPTION CONTROL REGISTER (CY2)		829
DATA ENCRYPTION CONTROL REGISTER (CY1)		828
DATA ENCRYPTION CONTROL REGISTER (CY0)		827
TSS SIZE	E	826 825-1
I/O MAP BASE ADDRESS	U R T	825 825-2
LDT SEGMENT SELECTOR		824
GS		823
FS		822
DS		821
SS		820
CS		819
ES		818
EDI		817
ESI		816
EBP		815
ESP		814
EBX		813
EDX		812
ECX		811
EAX		810
EFLAGS		809
EIP		808
CR3(PDBR)		807
SS2		806
ESP2		805
SS1		804
ESP1		803
SS0		802
ESP0		801
LINK TO PREVIOUS TASK		

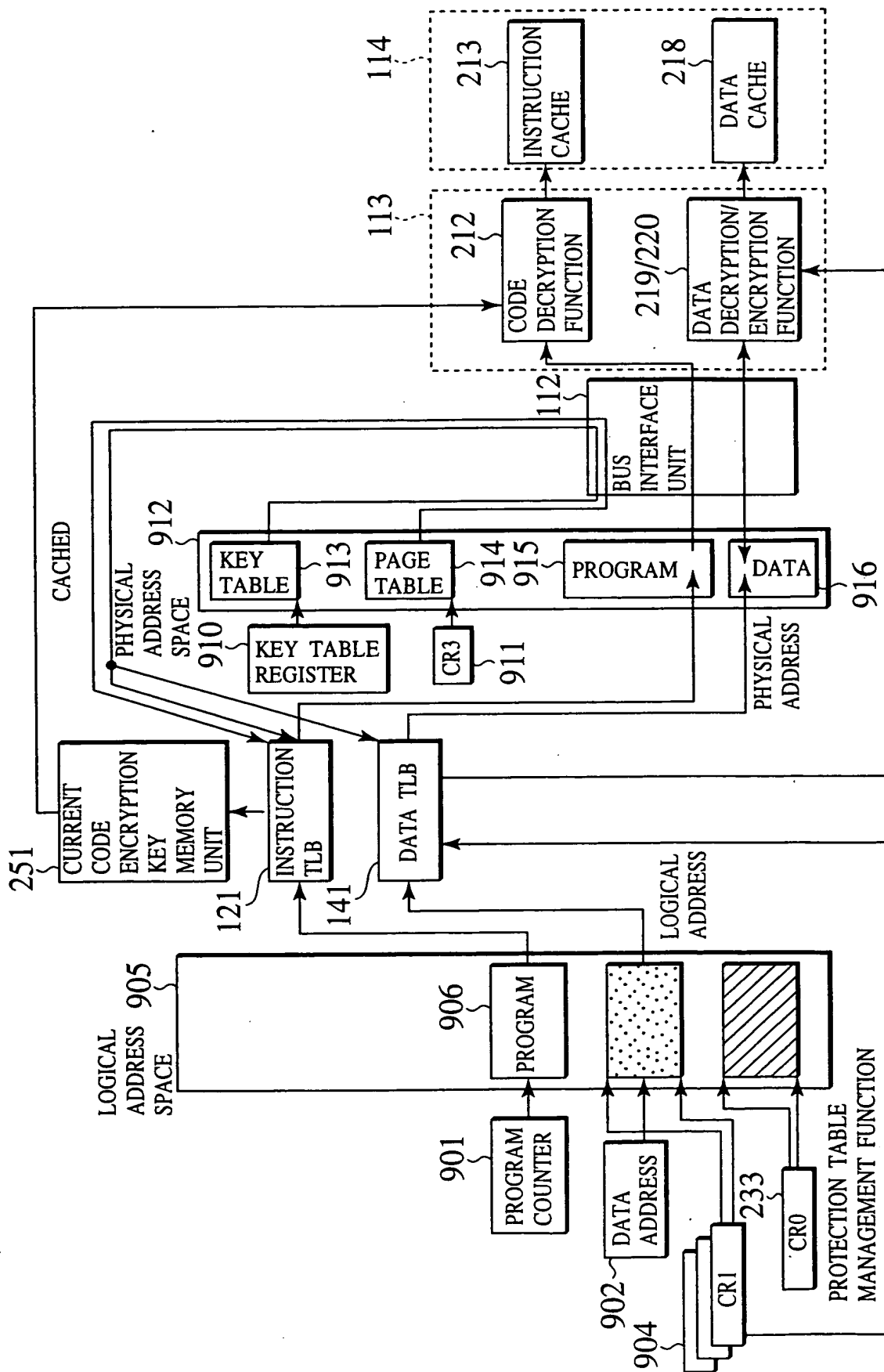
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FIG.11



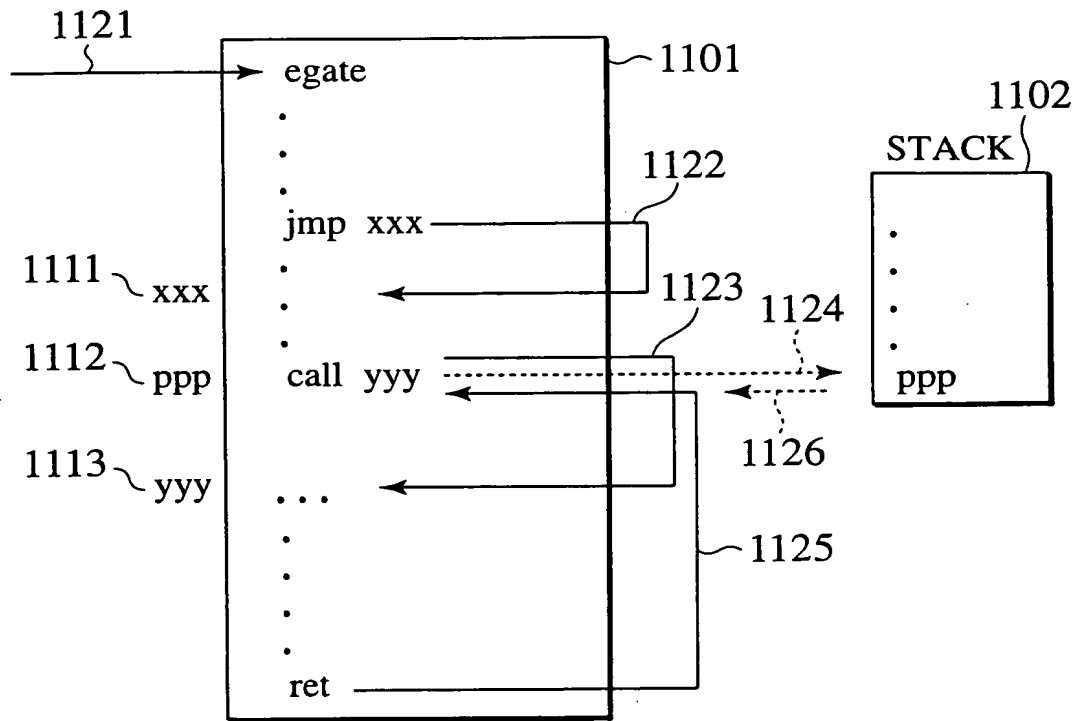
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FIG.12



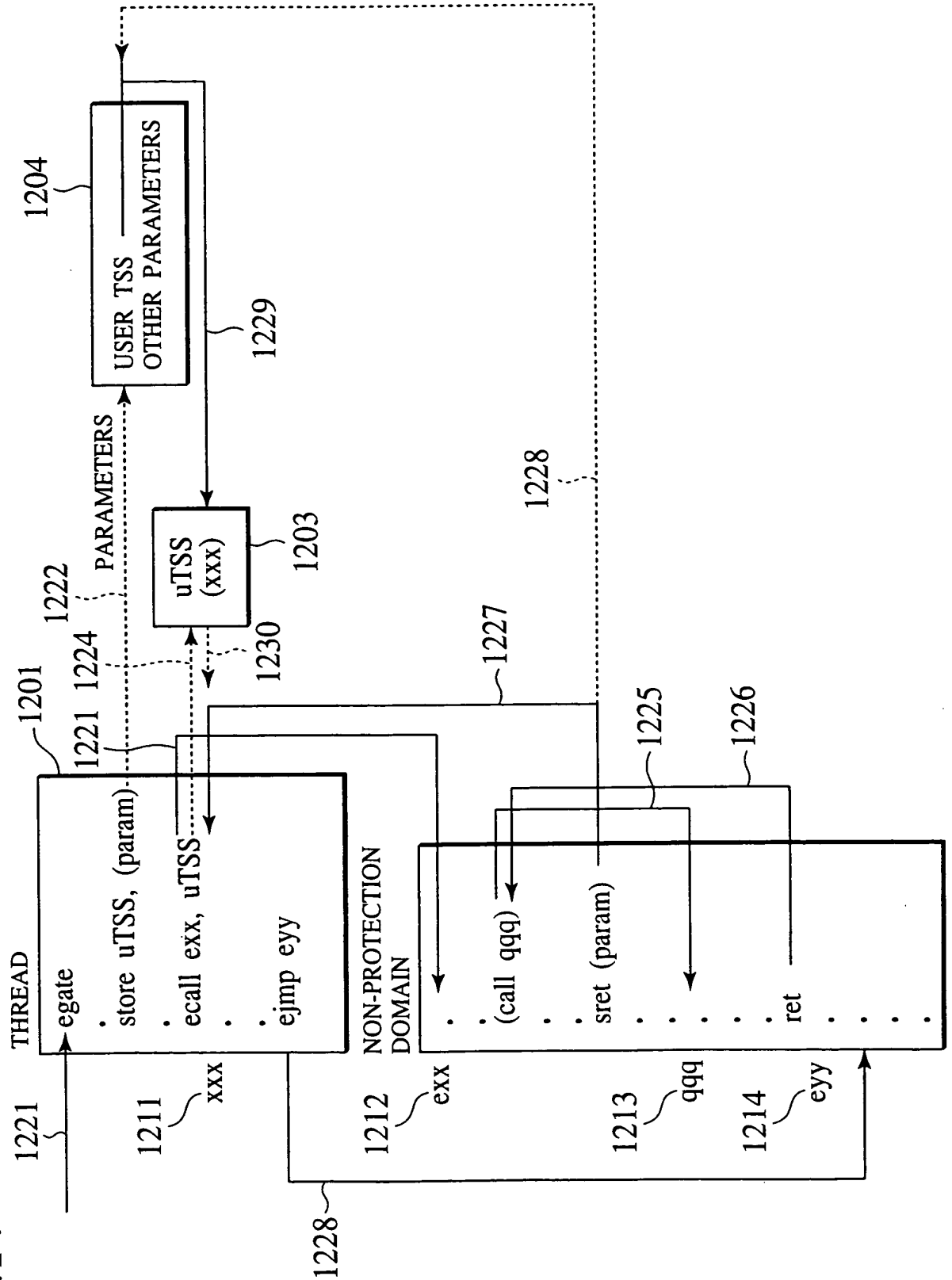
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FIG.13



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FIG.14



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FIG.15
PRIOR ART

31	15	0
I/O MAP BASE ADDRESS		T 100
	LDR SEGMENT SELECTOR	96
	GS	92
	FS	88
	DS	84
	SS	80
	CS	76
	ES	72
	EDI	68
	ESI	64
	EBP	60
	ESP	56
	EBX	52
	EDX	48
	ECX	44
	EAX	40
	EFLAGS	36
	EIP	32
	CR3(PDBR)	28
	SS2	24
	ESP2	20
	SS1	16
	ESP1	12
	SS0	8
	ESP0	4
	LINK TO PREVIOUS TASK	0